

**AMENDMENTS TO THE SPECIFICATION**

Please replace the following paragraphs.

[0005] The end product of the system 10 is the output data 34 during normal operation or scan out data 36 in the case of a scanning test sequence. Each of these system outputs 34 and 36 passes through a clocked storage device 38 and 40 in the I/O ring 12. Since the clock signal in the I/O ring ~~[[37]]~~ 12 is generally much slower than the clock signal in the core 14, a need exists to synchronize the data 34 from the core 14 with the system clock signal 16.